

J1000 U.S. PTO
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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10051886	FILING DATE 01/16/2002	CLASS 28	SUBCLASS 1	GAU 2827	EXAMINER CRUZ
**APPLICANTS: Felmetzger Valery; <i>Chambers</i>					
**CONTINUING DATA VERIFIED: AC None					
** FOREIGN APPLICATIONS VERIFIED: AC None					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/> RESCIND <input type="checkbox"/>					
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no Verified and Acknowledged Examiners's initials <i>AC</i>				ATTORNEY DOCKET NO SPUTT-57354	
TITLE : Permanent adherence of the back end of a wafer to an electrical component or sub-assembly <small>U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)</small>					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	
		Print Claim for O.G.	
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner	
		Application Examiner	
		PREPARED FOR ISSUE	
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